



1374.43386X00

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. WADA, et al.  
Serial No.: 10/765,917  
Filed: JANUARY 29, 2004  
For: FABRICATION METHOD OF SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE  
Group: 3729  
Examiner: Paul D. Kim  
Confirm. No: 4104

**AMENDMENT**

**Mail Stop: AMEND – NO FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 18, 2006

Sir:

In response to the Office Action mailed October 18, 2006, please amend the  
above-identified application as listed below and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.